

<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>			ATTY. DOCKET NO. <b>57454-970</b>	SERIAL NO. <b>Divisional of Appln. Serial No. 09/813,796</b>		
			<b>APPLICANT</b> <b>Hideto HIDAKA</b>			
<b>(PTO-1449)</b>			FILING DATE <b>August 21, 2003</b>	GROUP <b>Not yet assigned</b>		
<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS <i>cy</i>	CITE NO.	Document Number Number-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		US 6,414,489	07/2002	Chen et al.		
		US 6,107,134	08/2000	Lu et al.		
		US 5,956,279	09/1999	Mo et al.		
		US 5,694,364	12/1997	Morishita et al.		
		US 5,379,260	01/1995	McClure.		
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<b>FOREIGN PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes -Number & -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation
						Yes      No
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
		"A Precise On-Chip Voltage Generator for a Gigascale DRAM with a Negative Word-Line Scheme", by Tanaka et al., IEEE Journal of Solid-State Circuits, Vol. 34, No. 8, 8/1999, pp. 1084-1090.				
		"Ultra LSI Memory" by Kiyoo Ito, Advanced Electronics Series, November 5, 1994, published by Baifukan, pp. 351-371.				
		"An Experimental 256-Mb DRAM with Boosted Sense-Ground Scheme", by Asakura et al., IEEE Journal of Solid-State Circuits, Vol. 29, No. 11, 11/1994, pp. 1303-1309.				
EXAMINER <i>Cyber</i>			DATE CONSIDERED <i>4/04</i>			

\*EXAMINER: Initial if reference considered whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.